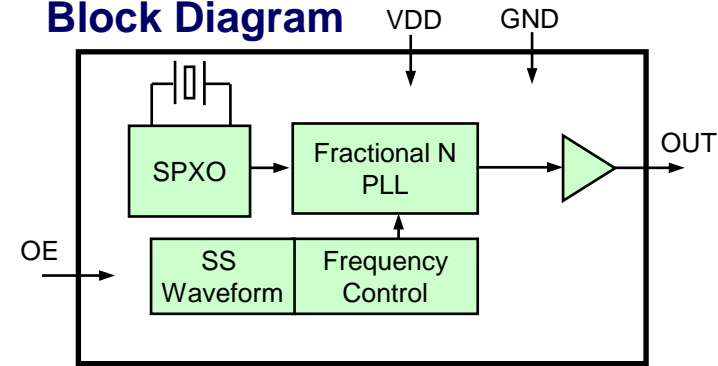


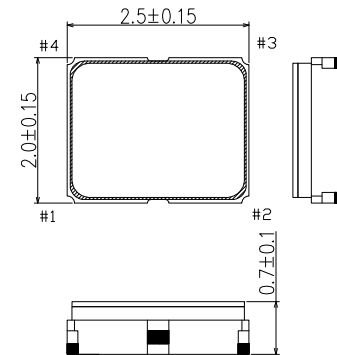
Features

- Easy to make sample with SG-Writer II
 - Programmable Output Frequency from 0.67M to 170MHz
 - Programmable Frequency Resolution 1ppm
 - Programmable Spreading percentage
 - Selectable Spread spectrum profile
- Wide operating temperature range from -40 to +105 °C
- LVCMOS output

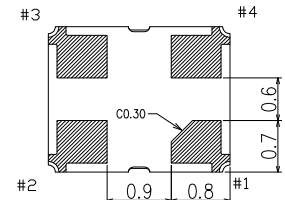
Block Diagram



Package and Pin Assignments



ex.2.5 x 2.0 Package



Pin map

Pin	Connection
1	OE or \overline{ST}
2	GND
3	OUT
4	Vcc

Schedule

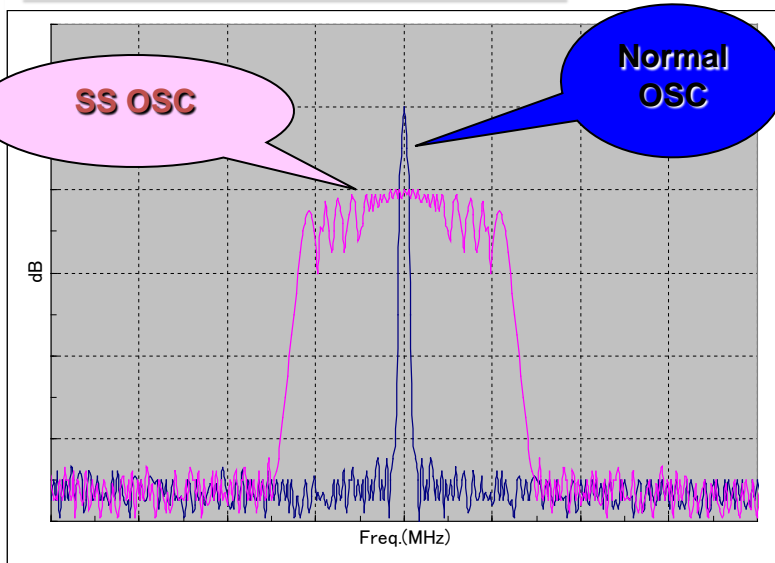
Size 7050~3225 ES Jun.'16 MP Jul.'16
 Size 2520 ES Jun.'16 MP Jun.'16
 Size 2016 TBD('16)

Item	Symbol	Specification
Output Frequency range	f _{OUT}	0.67M to 170MHz
Supply voltage	V _{CC}	1.62V ~ 3.63V
Operating Temperature	T _{use}	-40 to 105 °C
Spreading Percentage	f _{ss}	+/- 0.25% to +/- 2.0%
Spreading Profile	—	Triangle, Hershey-kiss, Sine
Function of PIN No.1	Func_PIN1	OE (ST Option)
Internal Crystal Frequency (f _{out} Initial Accuracy)	f _{REF}	26MHz Photo AT (Maximum variation is +/- 1 ppm)
Program Frequency setting resolution	f _{reso}	1ppm
Power Consumption (no load)	I _{DD}	3.6mA Max (f _{out} =20MHz) 7.8mA Max (f _{out} =170MHz) 3.7mA Max (Output disable)
Package Lineup	PKG	7050, 5032, 3225, 2520, 2016

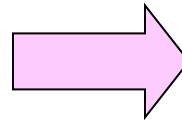
What is Spread Spectrum(SS)?

Change the clock frequency dynamically, to reduce the power of radiation.
Technic for EMI.

Comparison of spectrum: Normal OSC vs SS OSC



SS-OSC
SG-9101 series



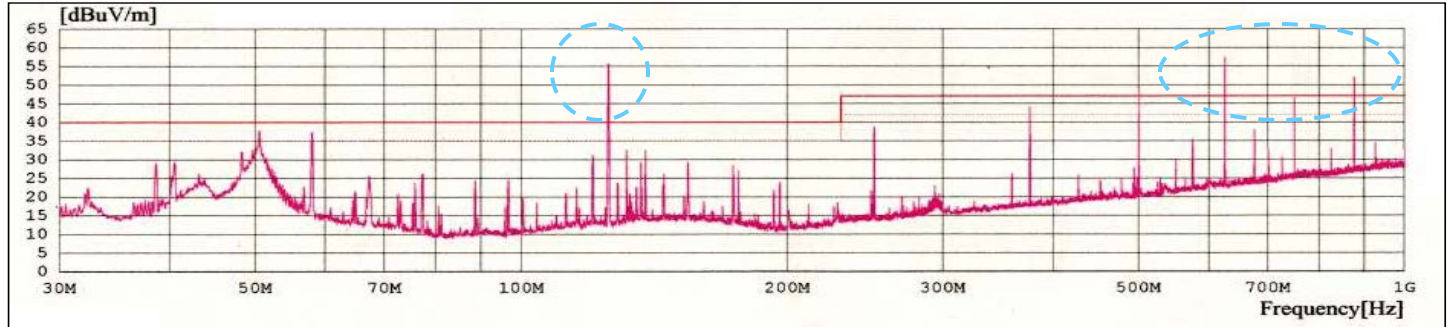
Reduce power radiation of
products

SS-OSC Output Spectrum

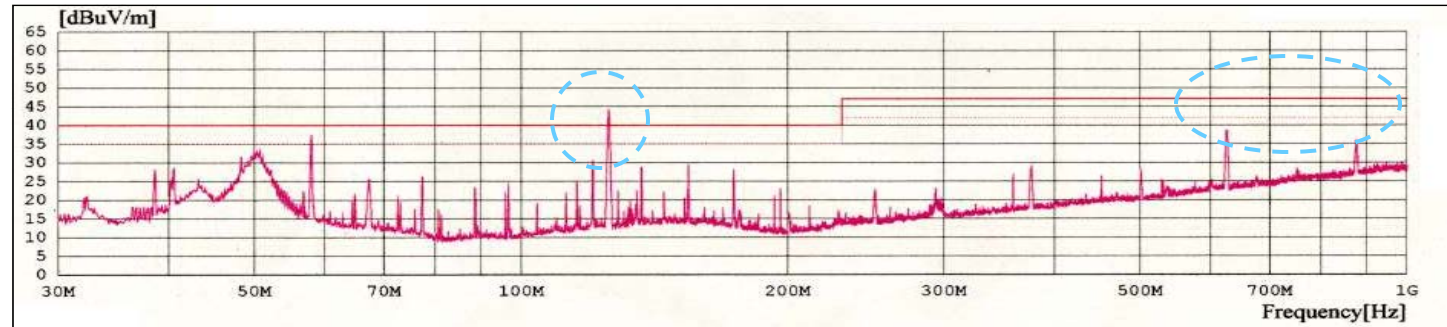
Reduce EMI

~Effect of high frequency~

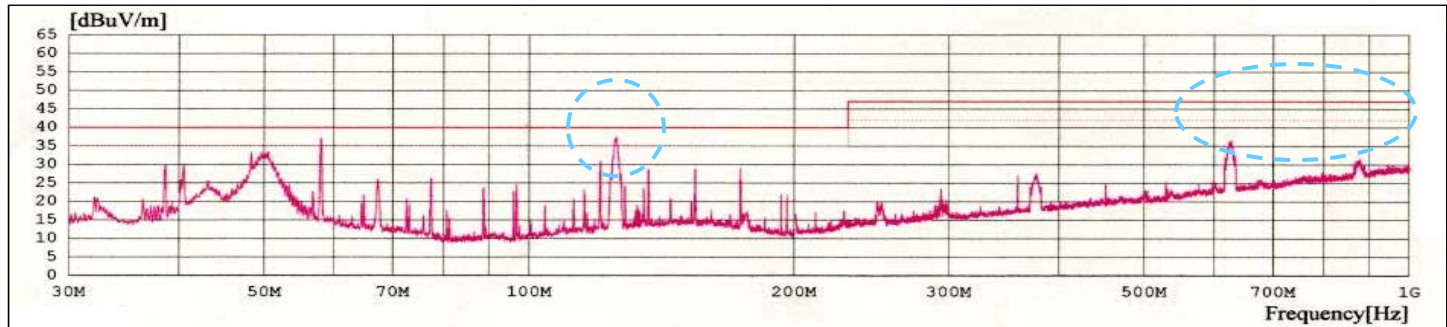
Spread OFF



Spread 0.5%



Spread 1.5%



■ User selectable configuration

Item	Spec	Comment
SS Direction	<u>Center Spread</u> / Down Spread	
SS Width	$\pm 0.25\% \sim \pm 2.0\%$ (Center Spread) - 0.5% ~ - 4.0%(Down Spread)	6 step
SS Period	6.3kHz ~ <u>25.4kHz</u>	4 step
SS Profile	<u>Hershey-Kiss</u> / Sine-Wave / Triangle	
Output Drive Level	2.5ns / 5.5ns / 10ns(max)	tr/tf@Cload=15pF

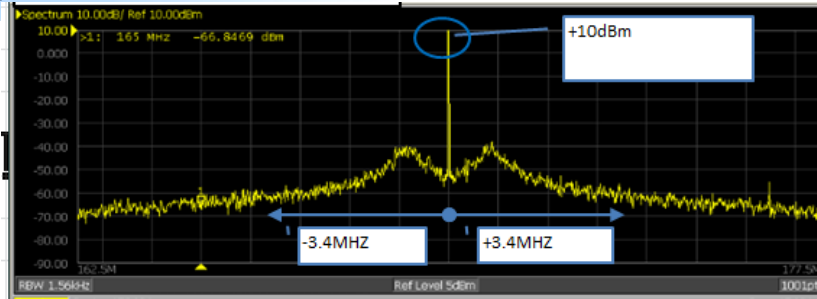
Flexible selection of SS configuration for each case

For example

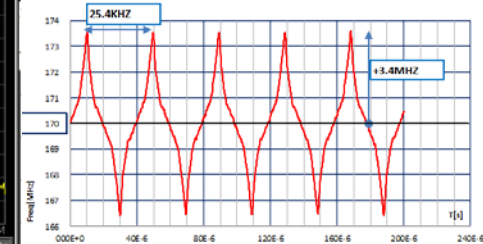
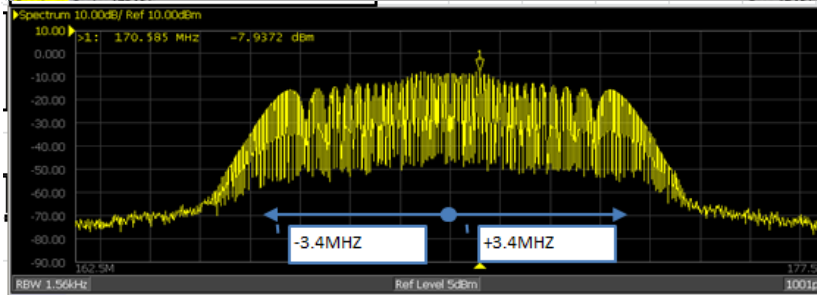
- Use for PLL reference clock ···slow SS Period and Sine-Wave Profile

Waveform Comparison

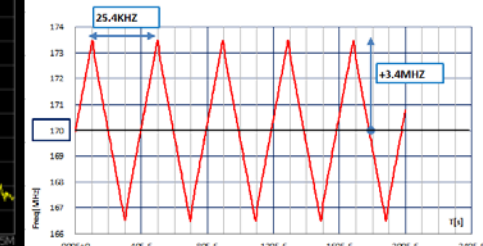
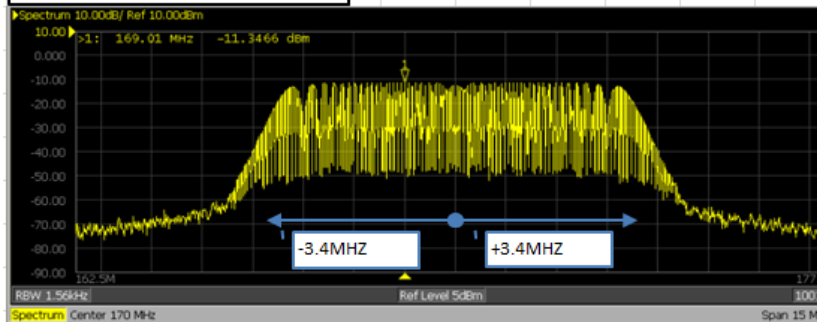
No spread spectrum



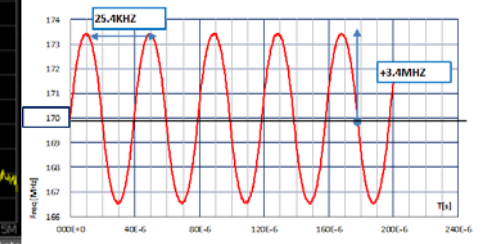
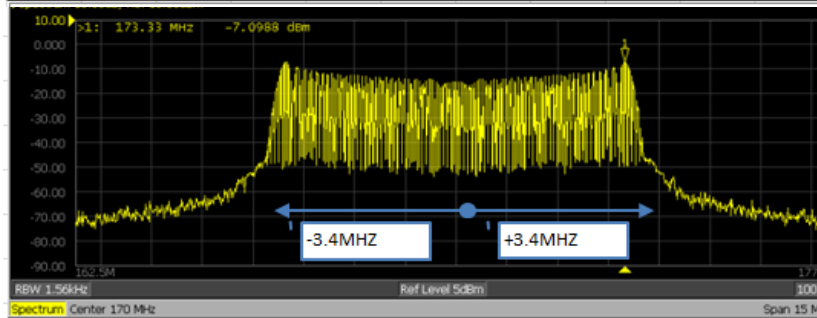
Hershey-kiss: -20 dB



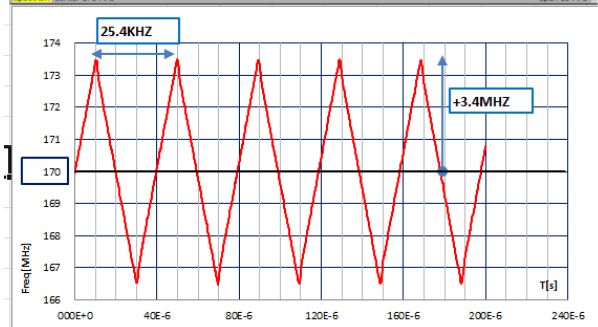
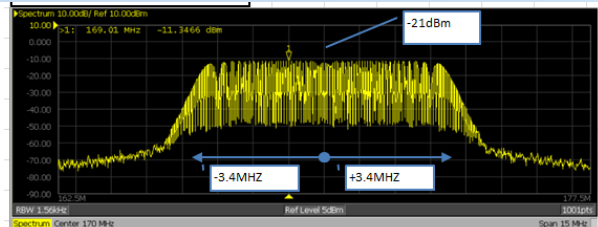
Triangle: -21 dB



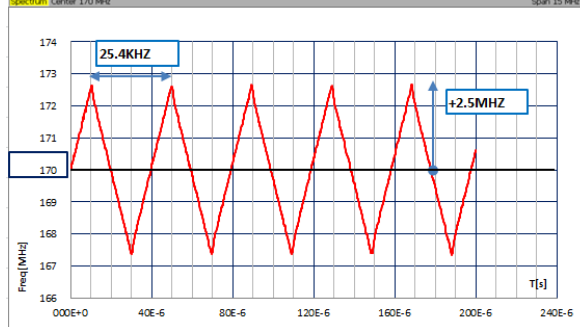
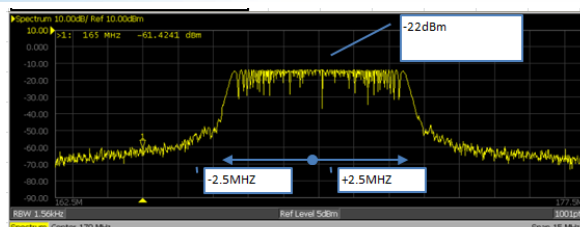
Sine: -17 dB



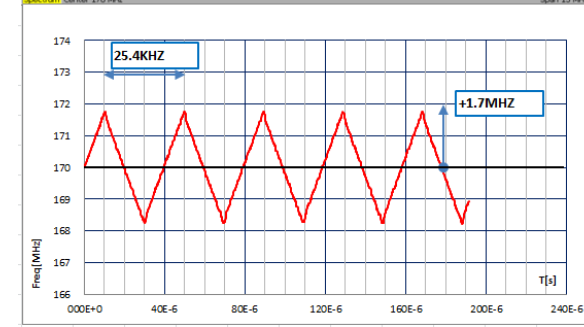
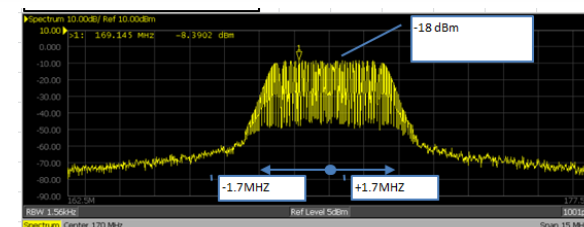
Deviation Comparison (Triangle)



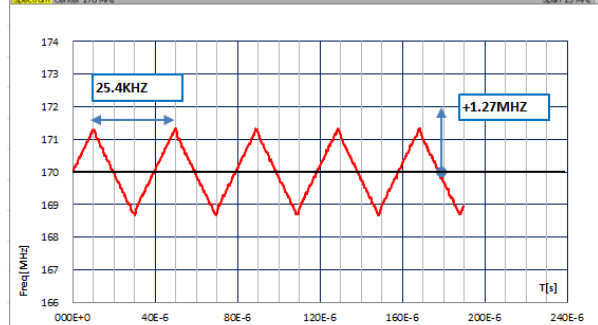
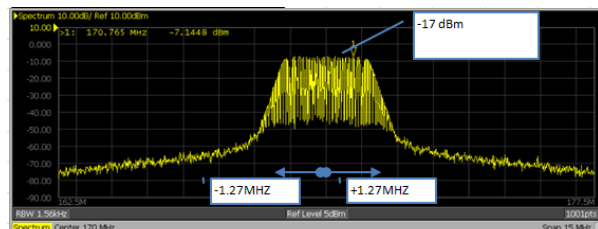
±2%: -21 dB



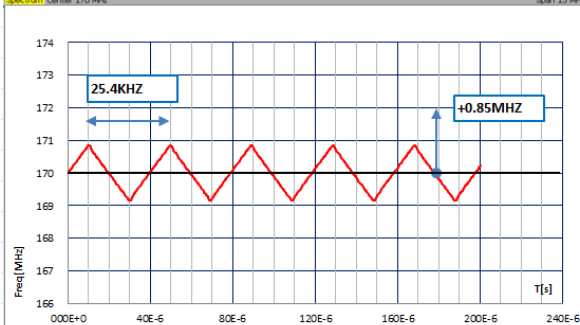
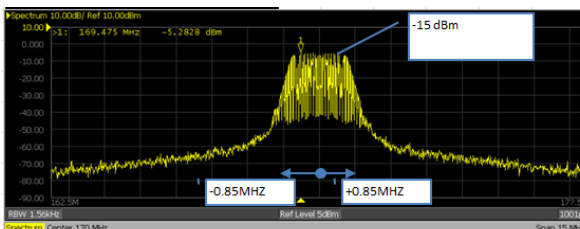
±1.5%: -22 dB



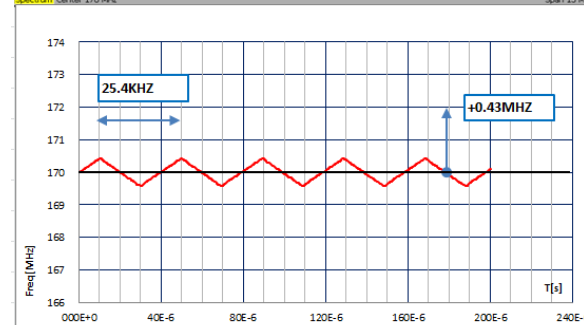
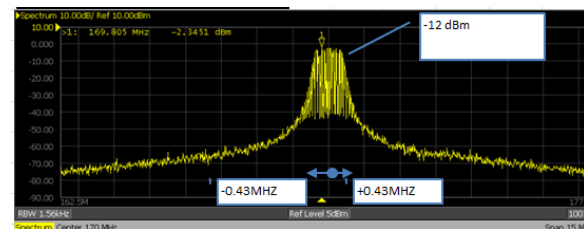
±1%: -18 dB



±0.75%: -17 dB



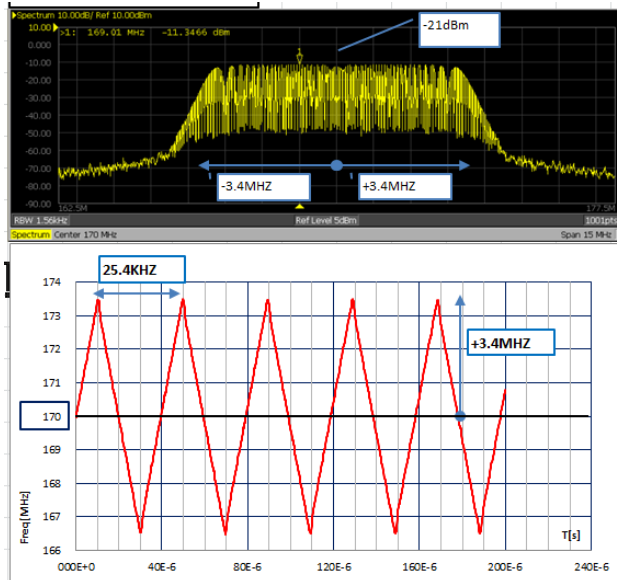
±0.5%: -15 dB



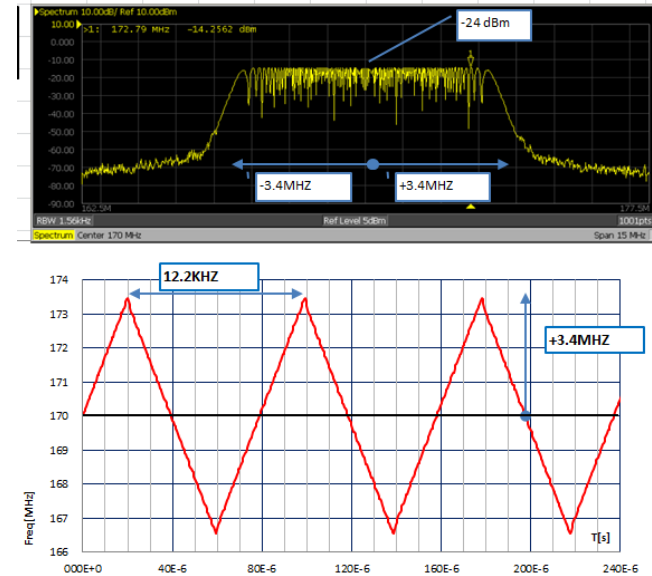
±0.25%: -12 dB

Rate Comparison (Triangle)

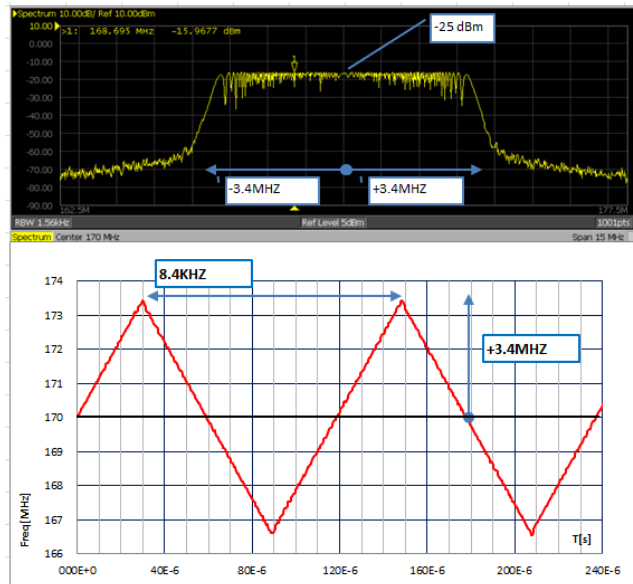
25.4 kHz:
-21 dB



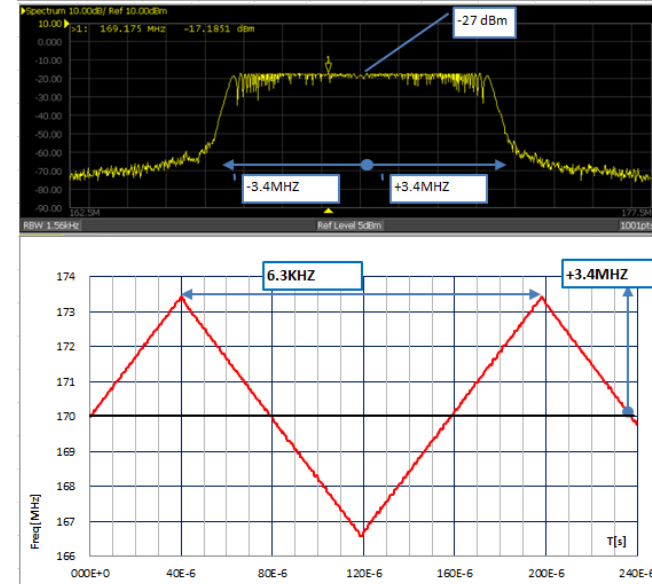
12.2 kHz:
-24 dB



8.4 kHz:
-25 dB



6.3 kHz:
-27 dB



SG-8503/4/6 Selectable / I²C Programmable PLL SPXO

Features

- Programmable Frequency from 50 to 800 MHz
- 2.5 V ~ 3.3 V, 90 mA Max.
- RMS jitter 0.3 ps Typ. (12 kHz ~ 20 MHz)
- Total tolerance +/-50 ppm
- Frequency pin select / I²C Interface
- Output Control (OE)
- Output Type:
LVPECL
- Output standby type: Hi-Z or Fixed

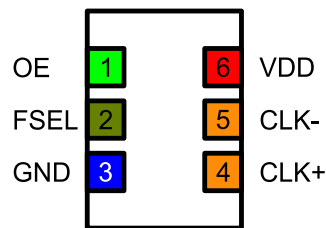
Technology

- Fundamental tone crystal oscillator (HFF)
- Low jitter fractional N PLL technology

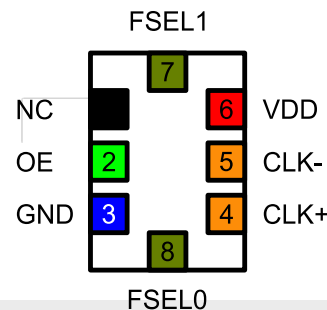
Package and Pin Assignments

- Small SMT Package 7.0 mm x 5.0 mm x 1.5 mm

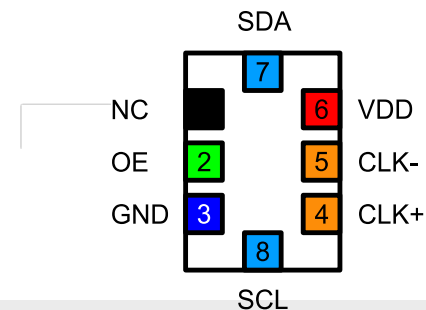
SG-8503CA
Dual Selectable



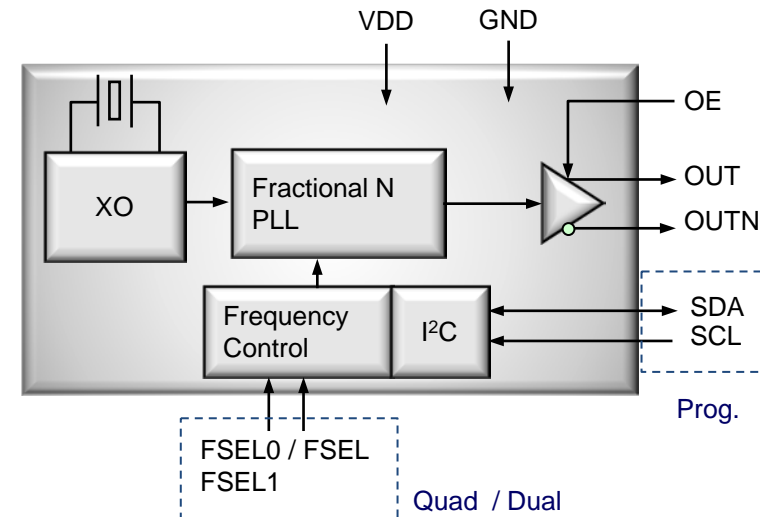
SG-8504CA
Quad Selectable



SG-8506CA
I²C programmable

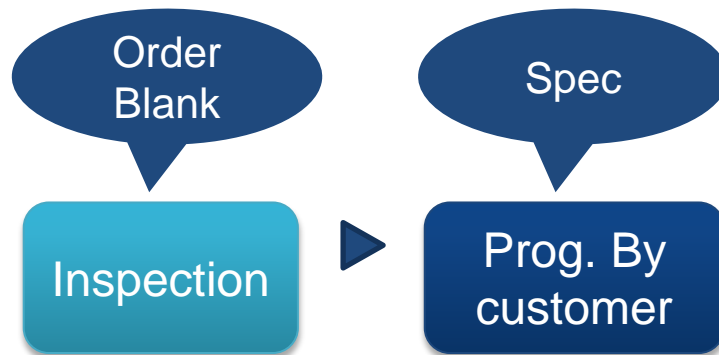


Block Diagram



Feature of SG-8101/9101 : Quick Sample Delivery

- Easy and quick samples : Our oscillator programmer (SG-Writer II) can program blank of SG-8101/9101 into any setting, profile, period, etc.
 - Anyone can make sample by SG-Writer II
- ## FROM ORDER TO DELIVERY



SG-Writer II

